



Management Dashboard

Overview

The Blue Pearl Software Visual Verification Suite provides enhanced Lint, Debug, Clock Domain Crossing (CDC) and automated SDC generation flows to accelerate RTL Verification. The Management Dashboard delivers improved visibility to ASIC, FPGA and IP RTL design rule and CDC checks to better assess schedules, risk and overall design quality.

Key Benefits

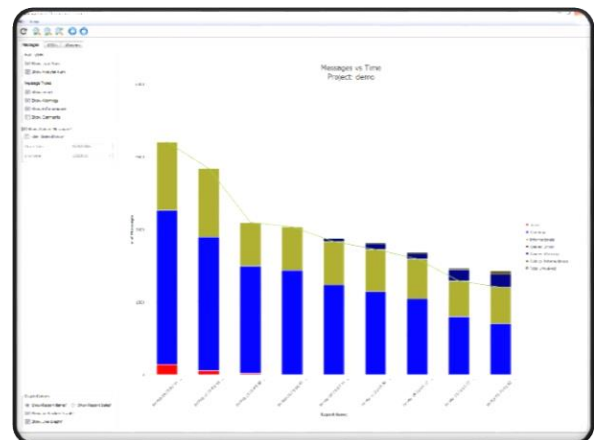
This standalone option to the Visual Verification Suite provides real-time visibility into RTL verification progress, run to run. It provides graphical project reports, over time for documentation, audits and design reviews. Reports highlight coverage, errors, warning and waivers for both Analyze RTL Linting and Clock Domain Crossing (CDC) solutions.

Reports can be customized to omit or show errors, warnings, comments and info comments allowing managers and designers to quickly customize focused reports on areas of interest. For FPGA designs, reports can also be generated to track key metrics as reported by the vendor tools such as power, area, maximum frequency, MTBF, and more. The tool works for both interactive and batch runs making it useful for individuals as well as design teams working on multiple ASIC or FPGA system designs.

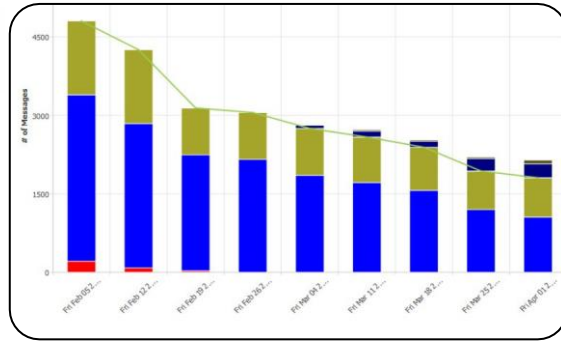
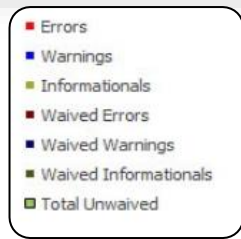
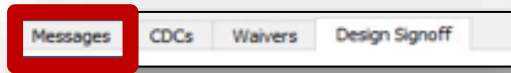
Management Dashboard Features

The Management Dashboard is built on top of the Visual Verification Suite's industry standard SQL database. The tool monitors and logs messages, Clock Domain Crossings and waivers per run to provide real-time visibility into RTL verification progress. The Signoff Report checks to see if the Analysis was run to company specific DRCs and the results were successful.

It is ideal to track and see what has been checked, what has been fixed, what has been waived and what still needs the designers focus.



Graphs from the Management Dashboard are easily exported in Microsoft Office tools for inclusion into documentation and standard reports making it ideal for program updates, audits and design reviews.



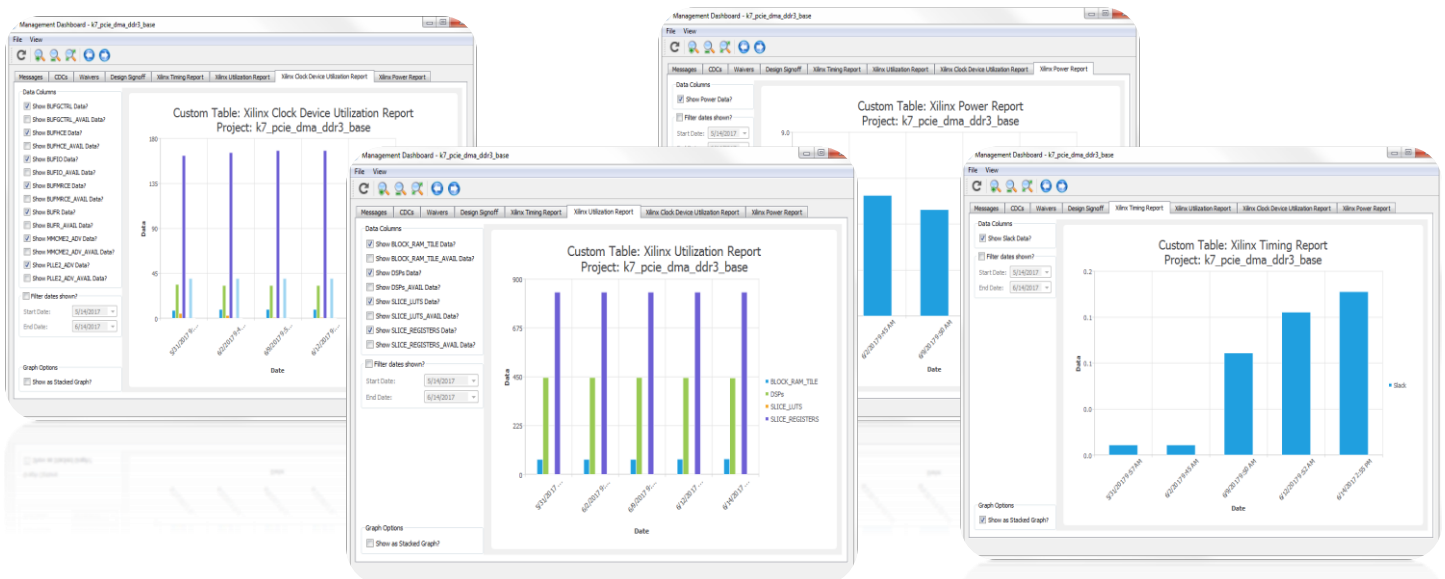
Management Waiver View

Xilinx Tcl Store App

Leveraging the power of the Xilinx Tcl Store, Blue Pearl Software has created an app that lets you place Blue Pearl icons in the Vivado toolbar to launch the Visual Verification Suite with the current project opened and ready for analysis and debug. The app also collects and formats vital run-to-run information from your implementation results to be displayed by the Management Dashboard, such as Period, Utilization, and Power so that designers can quickly assess whether key design metrics are being improved or degraded over time.

Management Dashboard

- **Track verification progress day to day and run to run**
 - Messages, CDCs and waivers per run
 - Custom reports
 - FPGA vendor metrics over time
 - Track both interactive and batch runs
- **Exports to Microsoft Word, Excel and PowerPoint**
- **Runs on both Windows and Linux operating systems**



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