



Management Dashboard

Overview

The Blue Pearl Solutions' Visual Verification Suite provides enhanced Lint, Debug, Clock Domain Crossing (CDC) and automated SDC generation flows to accelerate RTL Verification. The Management Dashboard delivers improved visibility to ASIC, FPGA and IP RTL design rule and CDC checks to better assess schedules, risk and overall design quality.

Key Benefits

This standalone option to the Visual Verification Suite provides real-time visibility into RTL verification progress, run to run, providing graphical project reports that can be customized for documentation and design reviews. Reports highlight coverage, errors, warning and waivers for both Analyze RTL Linting and Clock Domain Crossing (CDC) solutions.

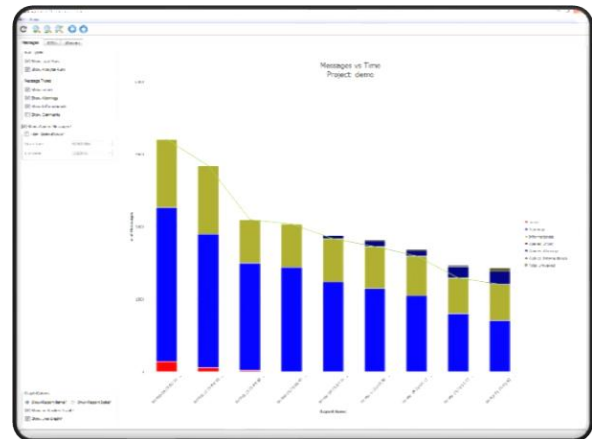
Management Dashboard Features

The Management Dashboard is built on top of the Visual Verification Suite's industry standard SQL database. The tool monitors and logs messages, Clock Domain Crossings and waivers per run to provide real-time visibility into RTL verification progress.

Reports can be customized to omit or show errors, warnings, comments and info comments, allowing managers and designers to quickly customize focused reports on areas of interest. The tool works for both interactive and batch runs making it useful for individuals

as well as design teams working on multiple ASIC or FPGA system designs.

Graphs from the Management Dashboard are easily exported in Microsoft Office tools for inclusion into documentation and standard reports making it ideal for program updates and design reviews.



Management Dashboard

- **Track verification progress day to day and run to run**
 - Messages, CDCs and waivers per run
 - Track both interactive and batch runs
- **Exports to Microsoft Word, Excel and PowerPoint**
- **Runs on both Windows and Linux operating systems**

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